

Application number 09/620,308
Amendment dated March 29, 2005
Reply to office action mailed October 29, 2004

PATENT

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings of claims in the application:

Listing of Claims:

Claim 1 (cancelled)

Claim 2 (currently amended) The method of claim + 18, wherein the a pulse width for each pulse in a signal is substantially the same.

Claim 3 (currently amended) The method of claim + 18, wherein the a time duration between signals is at least two times longer than a width of a pulse.

Claim 4 (currently amended) The method of claim + 18, wherein a zero signal corresponds to a sequence of two pulses.

Claim 5 (currently amended) The method of claim + 18, wherein a one signal corresponds to a sequence of three pulses.

Claim 6 (currently amended) The method of claim + 18, wherein an acknowledge signal corresponds to a sequence of four pulses.

Claim 7 (currently amended) The method of claim + 18, wherein a start signal corresponds to a sequence of five pulses.

Claim 8 (currently amended) The method of claim + 18, wherein each pulse in the sequence remains low for a same time interval.

Claim 9 (currently amended) The method of claim + 18, wherein each pulse in the sequence remains high for a same time interval.

Claim 10 (previously presented) A serial apparatus used to transmit and receive commands for a battery management unit, the serial apparatus comprising:
a port operable to transmit and receive commands using a single conductor, each command comprising a sequence of pulses; and

Application number 09/620,308
Amendment dated March 29, 2005
Reply to office action mailed October 29, 2004

PATENT

serial interface logic compatible with a serial protocol and operable to detect sequences of pulses on the port, interpret the sequences of pulses according to the serial protocol to determine corresponding battery management unit commands, and provide the corresponding battery management unit commands to a controller.

Claim 11 (previously presented) The apparatus of claim 10, wherein the pulse width for each pulse in a signal is substantially the same.

Claim 12 (previously presented) The apparatus of claim 10, wherein the time duration between signals is at least two times longer than a width of a pulse.

Claim 13 (previously presented) The apparatus of claim 10, wherein a zero signal corresponds to a sequence of two pulses.

Claim 14 (previously presented) The apparatus of claim 10, wherein a one signal corresponds to a sequence of three pulses.

Claim 15 (previously presented) The apparatus of claim 10, wherein an acknowledge signal corresponds to a sequence of four pulses.

Claim 16 (previously presented) The apparatus of claim 10, wherein a start communication signal corresponds to a sequence of five pulses.

Claim 17 (previously presented) A method of controlling a battery management unit, comprising:

transmitting a start command corresponding to a sequence of pulses to a battery management unit through a serial port;

receiving a first acknowledgement sequence of pulses from the battery management unit;

transmitting a command byte and an address associated with a byte of data to be accessed in memory to the battery management unit through the serial port as a sequence of pulses corresponding to a serial protocol; and

Application number 09/620,308
Amendment dated March 29, 2005
Reply to office action mailed October 29, 2004

PATENT

receiving a second acknowledgement sequence of pulses from the battery management unit.

Claim 18 (currently amended) ~~The method of claim 1, A method of~~
controlling a battery management unit, comprising:

receiving a sequence of pulses using a serial port, the sequence of pulses
consistent with a serial protocol; and
interpreting the received sequence of pulses according to the serial protocol,
wherein the serial protocol defines a set of battery management commands based
at least on a number of pulses in the sequence; the received sequence of pulses comprises a
battery management unit command, the command operable to control components within a
battery management unit; and the serial port consists of a single conductor;

wherein a the set of battery management unit command commands includes read/write commands, arithmetic commands, and interrupt commands.

Claim 19 (previously presented) The apparatus of claim 10, wherein a battery management unit command includes read/write commands, arithmetic commands, and interrupt commands.

Claims 20 and 21 (cancelled).

Claim 22 (currently amended) ~~The integrated circuit of claim 21 A battery~~
management integrated circuit comprising:

a serial port consisting of a single conductor;
a serial interface logic circuit coupled to the serial port; and
a controller coupled to the serial interface logic circuit,
wherein the serial port receives a first signal, the first signal transitioning between
a first level and a second level, the first signal comprising a first battery management command,
and

wherein the serial interface logic circuit interprets the first signal, determines the first battery management unit command, and provides the first battery management command to the controller.

Application number 09/620,308
Amendment dated March 29, 2005
Reply to office action mailed October 29, 2004

PATENT

Claim 23 (previously presented) The integrated circuit of claim 22 wherein the first signal transitions from the first level to the second level a first number of times for a first duration then remains at the first level for at least a second duration for each logic zero in the signal, and the first signal transitions from the first level to the second level a second number of times for the first duration then remains at the first level for at least the second duration for each logic one in the signal.

Claim 24 (previously presented) A battery management integrated circuit comprising:
a serial port configured to transmit and receive signals using a single conductor;
a controller configured to provide and accept commands; and
a serial interface circuit coupled between the serial port and the controller and configured to accept commands from the controller and provide signals for transmission by the serial port, and to accept signals from the serial port and provide commands to the controller.

Claim 25 (previously presented) The integrated circuit of claim 24 wherein the signals transition between a first level and a second level, the signals transition from the first level to the second level a first number of times for a first duration then remain at the first level for at least a second duration for each logic zero in the signals, and the signals transition from the first level to the second level a second number of times for the first duration then remain at the first level for at least the second duration for each logic one in the signal.

Claim 26 (new) A battery management integrated circuit comprising:
an interface control unit configured to receive battery management commands including memory read and write commands, and further configured to receive memory addresses and write data; and
a memory configured to receive the memory write commands, memory addresses, and write data from the interface control unit, and further configured to store the write data at the memory addresses received from the interface control unit.

Application number 09/620,308
Amendment dated March 29, 2005
Reply to office action mailed October 29, 2004

PATENT

Claim 27 (new) The integrated circuit of claim 26 wherein the interface control unit receives the battery management commands, memory addresses, and write data using a single conductor.

Claim 28 (new) The integrated circuit of claim 26 wherein the interface control unit is further configured to provide read data, and the memory is further configured to receive the memory read commands from the interface control unit and to retrieve the read data from the memory addresses received from the interface control unit.